

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,801,074 B2
APPLICATION NO. : 10/618726
DATED : October 5, 2004
INVENTOR(S) : Tetsumasa Meguro

Page 1 of 1

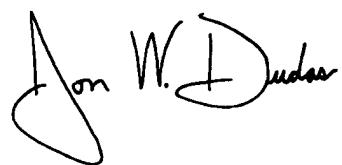
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12

Line 37, "delay clock" should read -- delays clock --.

Signed and Sealed this

Eighth Day of January, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office